

Central Glass & Ceramic Research Institute  
KOLKATA (WEST BENGAL) INDIA  
CORRIGENDUM

REFERENCE NO. :- P/NC/326/DK/DB(SB)/OTE/20-21

DATE: 22/03/2021

NAME OF EQUIPMENT: DATA ACQUISITION MODULE OF HIGH SPEED INTERROGATION SYSTEM FOR FBG SENSORS

NOTE: The Bids must be submitted in the Central Public Procurement Portal (URL:<https://etenders.gov.in/eprocure/app>) only. Manual/Offline bids shall not be accepted under any circumstances. Bidders should quote in INR only.

CONSEQUENT TO THE PRE-BID MEETING HELD ON 10/03/2021, THE REVISED SPECIFICATION IS GIVEN BELOW:-

SPECIFICATIONS:-

Details of Items*	Quantity
<p>Detailed specification: Data Acquisition module of high speed Interrogation System for FBG Sensors (4 channel unit) consisting of the following components:</p> <ol style="list-style-type: none"> <li>1. 3U PXI Express Chassis providing: <ul style="list-style-type: none"> <li>• 4-Slot</li> <li>• high-bandwidth backplane supporting data rate upto 3GB/s</li> </ul> </li> <li>2. PXI Controller Card: Embedded controller card compatible with PXI express systems with following specification requirement: <ul style="list-style-type: none"> <li>• minimum configuration of 2.8 GHz 4-Core</li> <li>• Integrated memory of 16 GB or more</li> <li>• Integrated solid state data storage of 512 GB or more</li> <li>• minimum port configuration of two 10/100/1000BASE-TX (Gigabit) Ethernet ports, two USB 3.0 ports, four USB 2.0 ports, serial port, two Thunderbolt 3 ports, and other peripheral I/O</li> <li>• supporting Windows 10 IoT Enterprise.</li> </ul> </li> <li>3. PXI Data Acquisition card: Data acquisition/ Oscilloscope card compatible with PXI express systems with the following specification requirement: <ul style="list-style-type: none"> <li>• minimum 8 channel simultaneous acquisition</li> <li>• sampling rate upto a minimum of 60 MHz per channel</li> <li>• minimum 12 bit resolution</li> <li>• Inbuilt anti-alias and noise filters</li> <li>• Independent channel selectable 50 ohm or 1Mohm input impedance</li> <li>• Minimum 512MB on board memory</li> <li>• compatible for ±10V analog input signal</li> <li>• suitable for input through BNC cables</li> </ul> </li> <li>4. PXI Multifunction I/O module: Analog and Digital Multifunction I/O card compatible with PXI express systems with the following specification requirement: <ul style="list-style-type: none"> <li>• 32 Single Ended/16 Differential Analog Input</li> <li>• minimum 16 bit resolution</li> <li>• +/- 10V input</li> <li>• sampling rate upto 250KS/s</li> <li>• minimum 48 Digital I/O</li> <li>• minimum 4 Analog Output,</li> <li>• +/- 10V output.</li> <li>• Update rate upto 833KS/s</li> <li>• suitable for signal input and output through BNC cables</li> </ul> </li> <li>5. LabView based Software developed on LabView 2018 platform: (the Source code of the software has to be provided) <ul style="list-style-type: none"> <li>• LabView based GUI for integrated control of all the cards</li> <li>• LabView Software for saving acquired <i>continuous</i> data from all input channels in ordered text files within a folder structure at speed upto 2 Msps.</li> </ul> </li> <li>6. Technical support for LabView for development work: <ul style="list-style-type: none"> <li>• Technical Support to CGCRI software team for LabView based development for these cards as and when required. (minimum upto three years after delivery)</li> </ul> </li> </ol>	01

THE REVISED DEADLINE FOR BID SUBMISSION END DATE AND DATE OF OPENING OF BIDS ARE AS FOLLOWS:

Last Date for Submission of Bids	05/04/2021 by 11:30 A.M. (IST)	Opening of Bids	06/04/2021 by 11.30 A.M. (IST)
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**BID Securing Declaration Form should reach this office by 05/04/2021.**

The above amendments shall amount to amendments of the relevant terms of our Bid Document for CGCRI Tender No. P/NC/326/DK/DB(SB)/OTE/20-21.

All other Tender terms and conditions remain unchanged.

Bidders should quote only in INR.



(R. Ray)

Controller of Stores & Purchase

आर. रे / R. Ray

भारतीय ग्लास एवं सेरामिक निरीक्षण संस्थान  
CONTROLLER OF STORES & PURCHASE

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